



CPU Specifications	
Firmware	Built-in for SoftPLC runtime software
Solid State Disk	64Mb (ver 4.x runtime) 16Mb (ver 2.x, 3.x runtime) Optional up to 256Mb
DRAM	16Mb
Back-up RAM	128 words (256kb), continuous charge, retains contents for 1 year unpowered
Web Server	Optional for ver. 2.x & 3.x
Programming language	Ladder, C/C++, and Java Language
Built-in COM ports	(1) RS-232C, (1) RS-232/485, switch selectable
Built-in Ethernet	10 Base T with 2 LED's for status
PC/104 interface	Supported (select appropriate base)
Processor	486 DX4-100
Status LED's	9 LED's for RUN status, communication status, and fault code detection
Built-In Remote I/O Port	Coaxial SoftPIPE connector (models SPLC-2A/R only)
System Location	Fits in CPU slot on Tealware CPU bases (cat. No. SBASE0x or PBASE04)
Current Consumption	SPLC-2 1.5A SPLC-2/R 1.8A
Dimensions	141mm High x 110mm Deep x 55.5mm Wide

Built-In Mode Keyswitch	
PROG (Program)	I/O Service is suspended. Program changes, download, etc. are permitted.
REM (Remote)	System mode is controllable with TOPDOC® for Run/Program, Remote Test, Remote Program
RUN	Program and I/O service are active. Mode changes, program changes, etc. are not permitted from TOPDOC.

Tealware CPU Discrete I/O Capacity		
CPU Model	Discrete Inputs	Discrete Outputs
SPLC-2-LT, SPLC-2/R-LT	128	128
SPLC-2-1K, SPLC-2/R-1K	up to 1024	up to 1024
SPLC-2-2K, SPLC-2/R-2K	up to 2048*	up to 2048
SPLC-2-8K, SPLC-2/R-8K	up to 8192*	up to 8192
* Models SPLC2-x (local) can only exceed 1024 total I/O if another type I/O is used as part of the system (eg: DeviceNet, Profibus, etc.)		
Note: Analog I/O capability is limited only by the hardware configuration chosen.		

