



Specifications	
Output channels	4 single-ended
Output ranges	0~10 V, 1~5 V, ±10 V 0~20 mA, 4~20 mA, ±20 mA
Resolution	14 bits
Accuracy	±0.2%FSR
Zero drift	+/- 0.1 μV/°C
Span drift	+/-30 PPM/°C
Isolation	2.5 KV optical isolation between input signals and CPU, channels not individually isolated
Internal current consumption	400 mA
Range selection	DIP switches, all channels must be same range
External voltage source	24 VDC required
External connections	20-pt. terminal block connector, max. wire size #14 AWG
Weight	390 g
Features	
	\$ Individual Channel Enable/Disable
	\$ Last State Hold or Clear (zero) Control

Terminal #	Signal	Wiring Diagram
		<p>Voltage Output</p>
1	CH1_V+	<p>Current Output (working range of load impedance is 0~550 Ω)</p>
2	CH1_V+	
3	CH1_I+	
4	CH1_I-	
5	CH2_V+	
6	CH2_V-	
7	CH2_I+	
8	CH2_I-	
9	CH3_V+	
10	CH3_V-	
11	CH3_I+	
12	CH3_I-	
13	CH4_V+	
14	CH4_V-	
15	CH4_I+	
16	CH4_I-	
17	FG	
18	FG	
19	24V	
20	GND	

